

CLAIMS:

1. An image processing apparatus comprising:
central processing means for conducting
operation control of the whole image processing apparatus;
setting means for storing control information
specified by said central processing means;
clock generation means for generating a clock
having a basic period equivalent to that of a pixel or
less;
a plurality of variable frequency generation
means for adjusting a frequency of the clock outputted
from said clock generation means to a predetermined
level independently of each other, based on the control
information specified by said central processing means,
said plurality of variable frequency generation means
being provided respectively in association with a
plurality of development colors;
image input connection means for receiving
predetermined data from an external device;
a plurality of image processing means for
converting parallel image data inputted from said image
input connection means to serial image data, based on a
frequency of a clock outputted from associated one of
said variable frequency generation means, said plural-
ity of image processing means being provided respec-
tively in association with a plurality of development
colors; and

image output connection means for transferring the serial image data to an external device.

2. An image processing apparatus comprising:
central processing means for conducting operation control of the whole image processing apparatus;

setting means for storing control information specified by said central processing means;

clock generation means for generating a clock having a basic period equivalent to that of a pixel or less;

a plurality of variable frequency generation means for adjusting a frequency of the clock outputted from said clock generation means to a predetermined level independently of each other, based on the control information specified by said central processing means, said plurality of variable frequency generation means being provided respectively in association with development colors other than one predetermined color;

image input connection means for receiving predetermined data from an external device;

a plurality of image processing means for converting parallel image data inputted from said image input connection means to serial image data, based on a frequency of the clock outputted from said clock generation means and a frequency of a clock outputted from associated one of said variable frequency generation means by taking the frequency of the clock output-

ted from the clock generation means as a reference,
said plurality of image processing means being provided
respectively in association with all development
colors; and

image output connection means for transfer-
ring the serial image data to an external device.

3. An image processing apparatus according to
claim 2, wherein said plurality of image processing
means are adapted to conduct image data addition/
removal processing operation, and said central
processing means has control information to control at
least one of the processing operation of said plurality
of image processing means and the frequency adjusting
operation of said variable frequency generation means.